

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09-917,573	GROUP/ART UNIT 2817	ATTACHMENT TO PAPER NUMBER 6			
NOTICE OF REFERENCES CITED				APPLICANT(S) Marra et al.					
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*		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE		
	A	4107621	8-15-1978	Furutani et al.	330	296			
	B	4123722	10-31-1978	Cubbison Jr.	330	85			
	C	4320352	3-16-1982	Rubin et al.	330	277			
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	L	54005366	1-16-1979	Japan	Hirayasu	—	—		
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	R	Yu et al. "An Automatic Offset Compensation Scheme with Ping-Pong control for CMOS Operational Amplifiers, IEEE Journal of Solid-State Circuits Volume 29 Issue 5 May 1994 pp 601-610							
	T	Millman "Micro-Electronics Digital and Analog Circuits and Systems" McGraw-Hill Copyright 1979 pp 523-527							
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